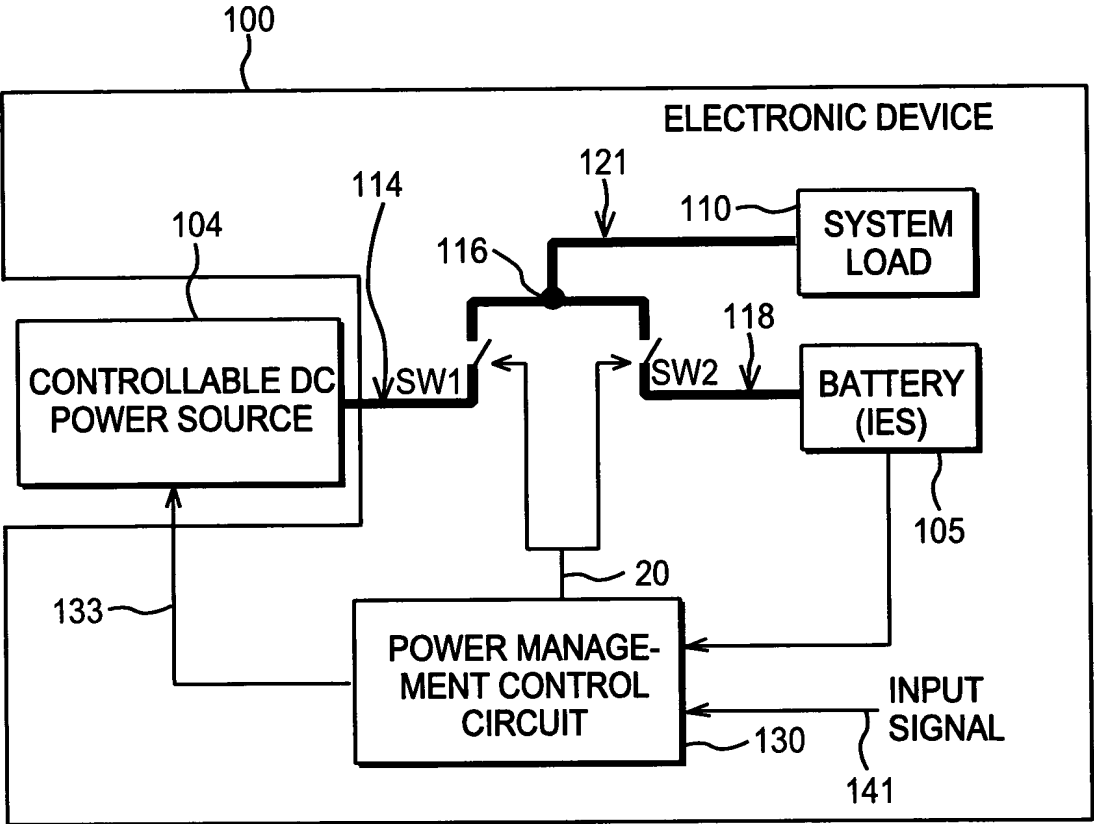


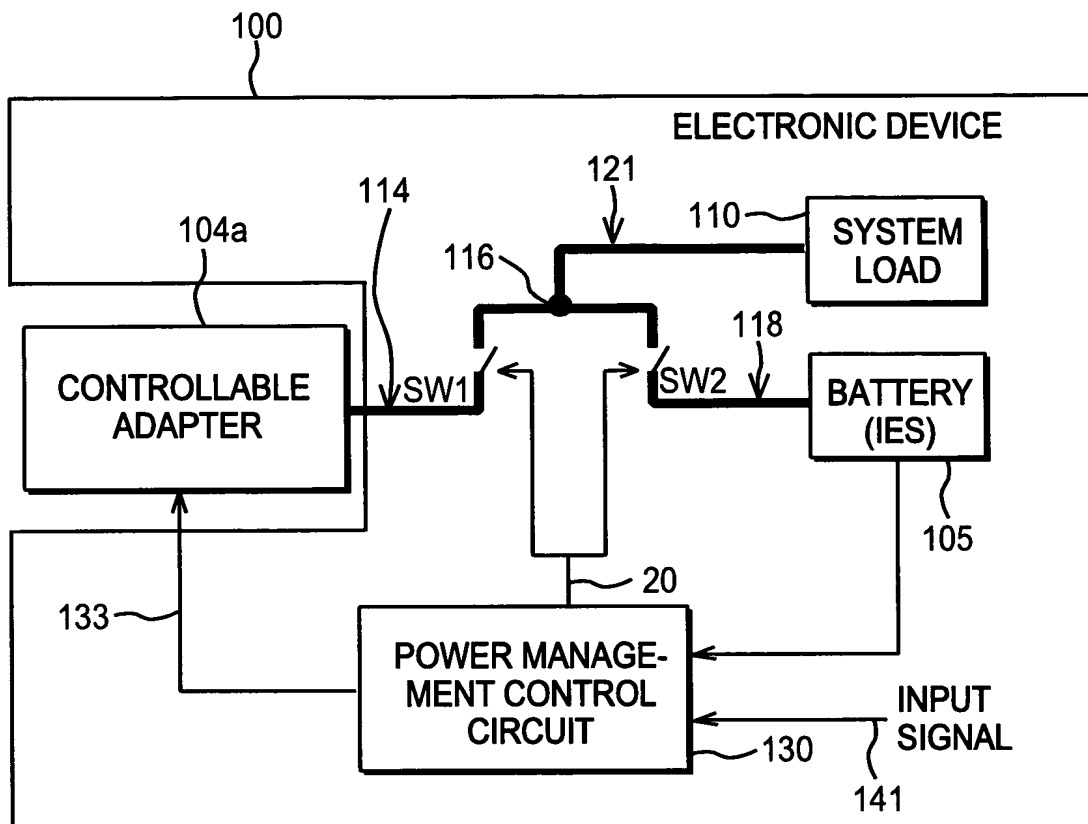


FIG. 1



SUPPLY MODE	SW1	SW2	
POWER BY DC NO CHARGE	ON ↑	OFF	← 181
POWER BY DC CHARGE	ON ↑	ON ↓	← 183
BUFFER BATTERY	ON ↑	ON ↑	← 185
POWER BY BATTERY	OFF	ON ↑	

# FIG. 2



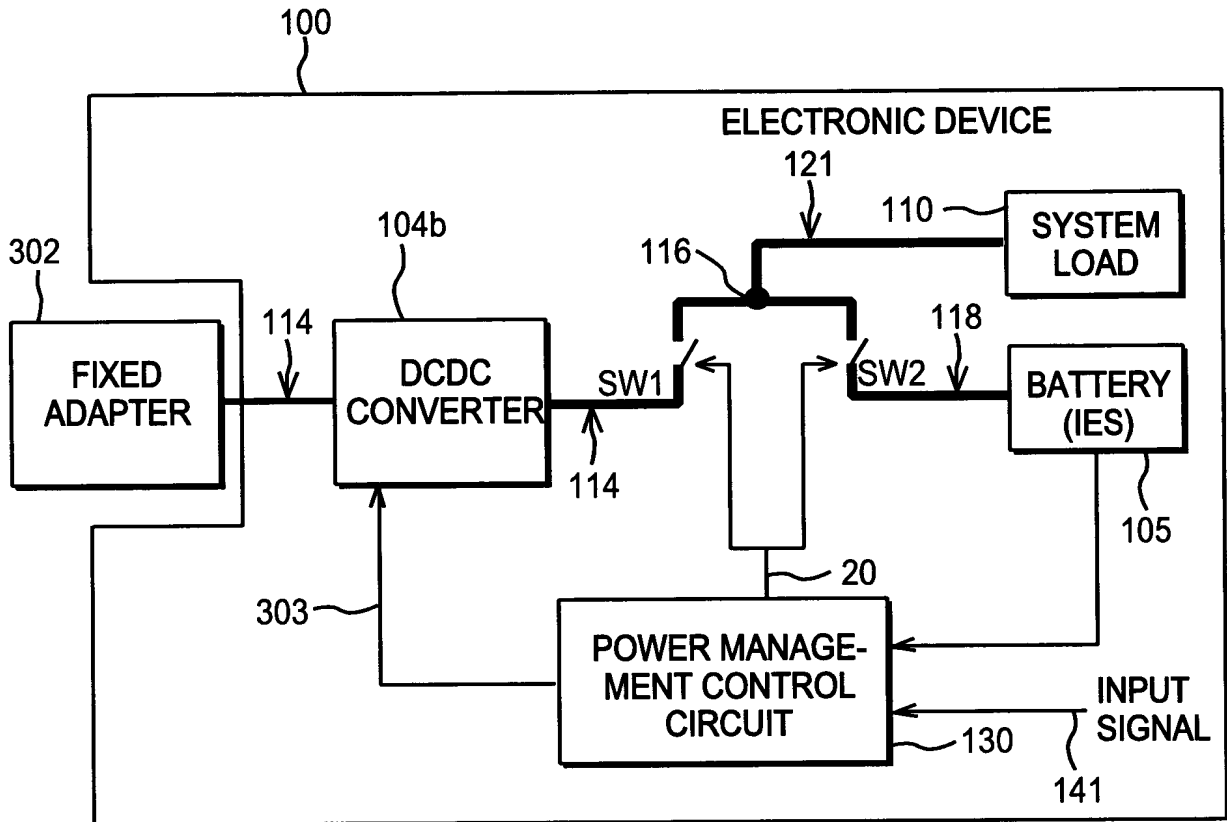
180

SUPPLY MODE	SW1	SW2
POWER BY DC NO CHARGE	ON ↑	OFF
POWER BY DC CHARGE	ON ↑	ON ↓
BUFFER BATTERY	ON ↑	ON ↑
POWER BY BATTERY	OFF	ON ↑

190

SUPPLY MODE	SW1	SW2
POWER BY DC NO CHARGE	ON ↑	OFF
POWER BY DC CHARGE	ON ↑	ON ↓
BUFFER BATTERY	NOT AVAILABLE	
POWER BY BATTERY	OFF	ON ↑

# FIG. 3



SUPPLY MODE	SW1	SW2
POWER BY DC NO CHARGE	ON ↑	OFF
POWER BY DC CHARGE	ON ↑	ON ↓
BUFFER BATTERY	ON ↑	ON ↑
POWER BY BATTERY	OFF	ON ↑

180

185

FIG. 4

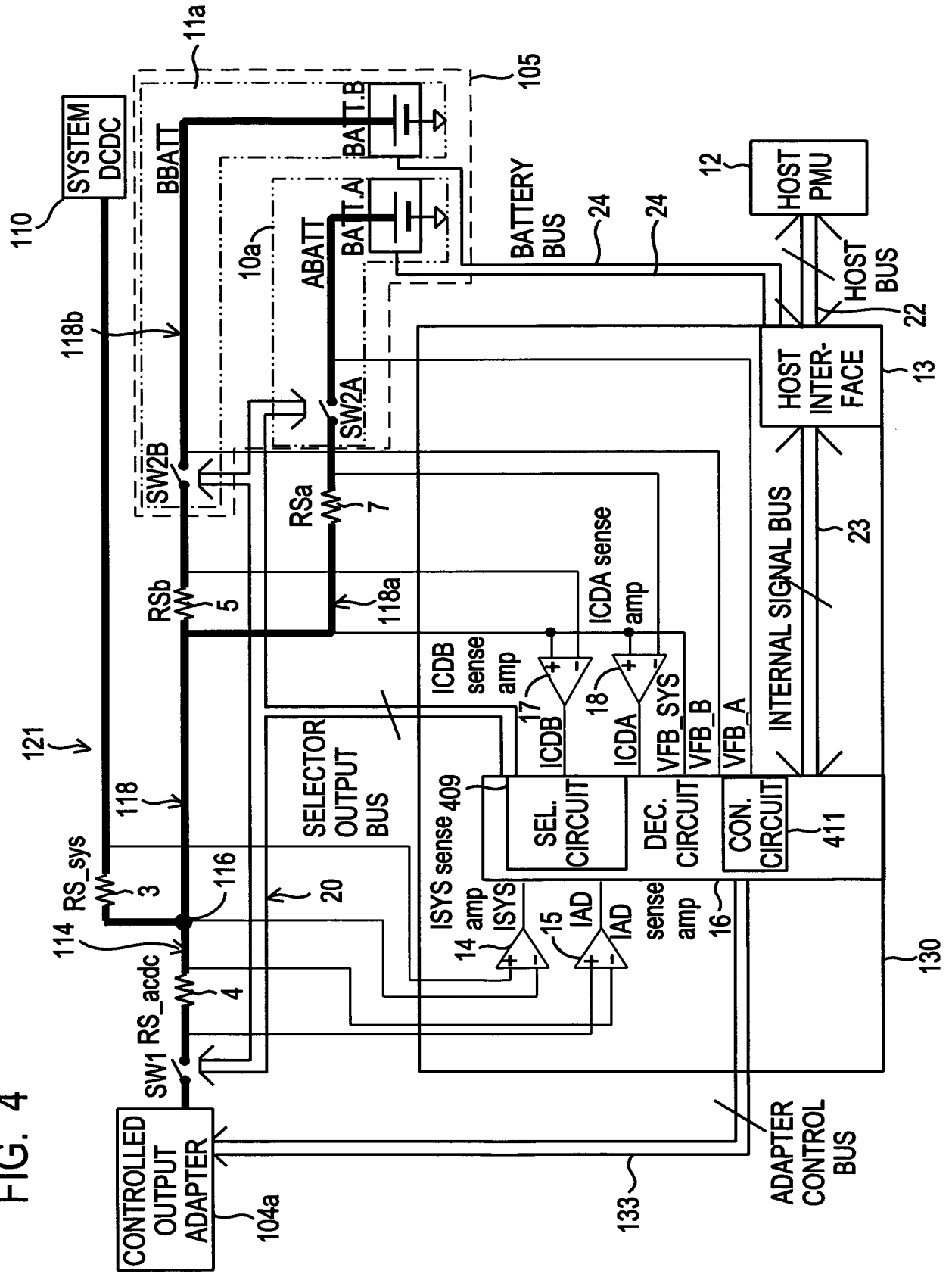
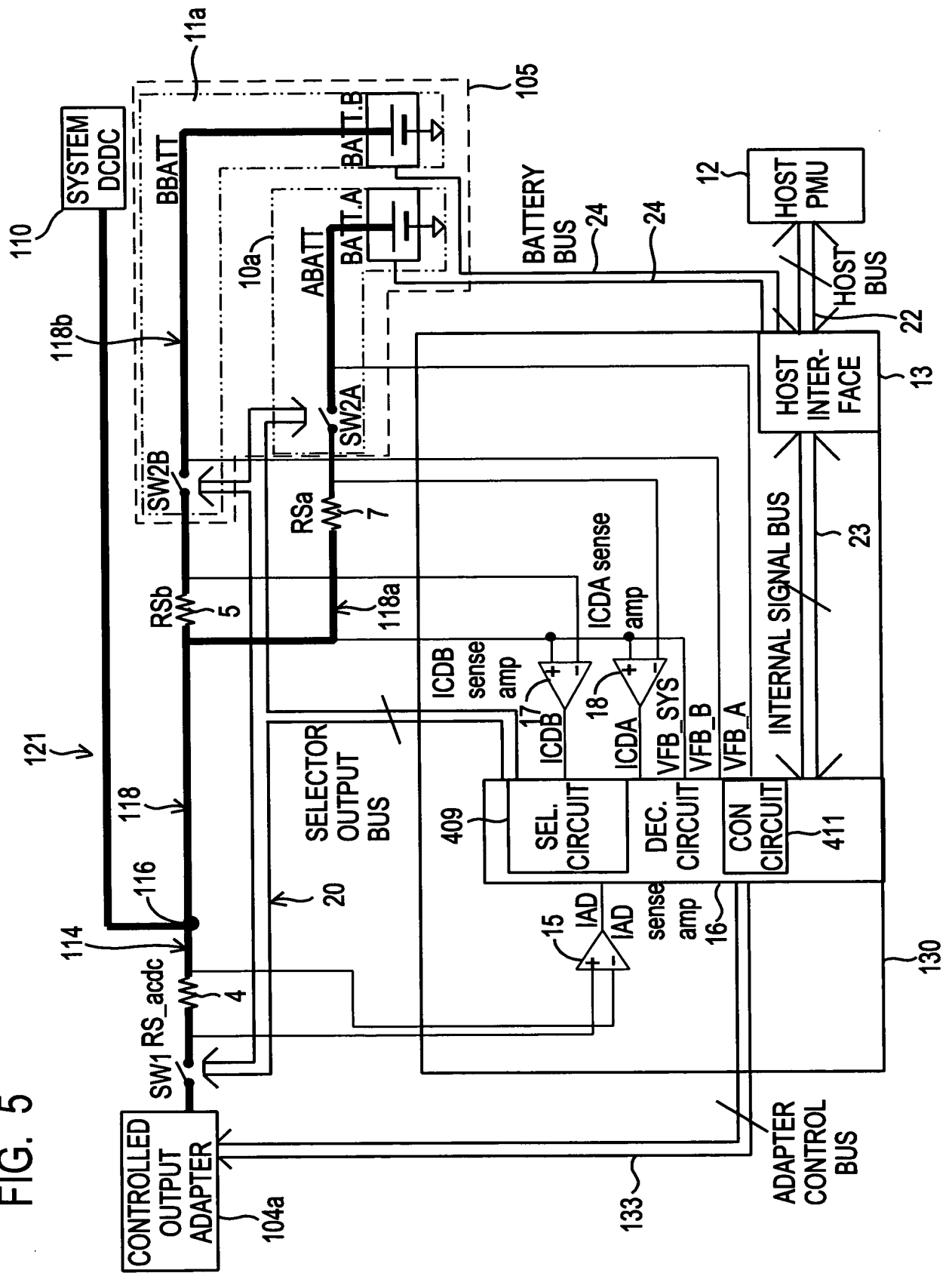


FIG. 5

FIG. 5 is a block diagram of a controlled output adapter system. The system includes a **CONTROLLED OUTPUT ADAPTER** (104a) connected to a **SYSTEM DCDC** (110) via a switch **SW1** (114) and a resistor **RS<sub>acdc</sub>** (4). The adapter contains a **SELECTOR OUTPUT BUS** (20) with a resistor **RS<sub>b</sub>** (5) and a switch **SW2B** (118b). It also includes a **SELECTOR CIRCUIT** (409) with an **IAD sense amp** (15) and a **DEC. CIRCUIT** (411) with an **IAD sense amp** (16). The adapter is connected to a **BATTERY BUS** (24) via a switch **SW2A** (118a) and a resistor **RS<sub>a</sub>** (7). The battery bus is connected to a **BATTERY** (105) with terminals **BATT.A** and **BATT.B**. The adapter also includes a **HOST INTERFACE** (13) connected to a **HOST PMU** (12) via a **HOST BUS** (22). The adapter control bus (133) is connected to the adapter control bus (130). The adapter also includes a selector circuit (409) with an IAD sense amp (15) and a dec. circuit (411) with an IAD sense amp (16). The adapter is connected to a BATTERY BUS (24) via a switch SW2A (118a) and a resistor RS<sub>a</sub> (7). The battery bus is connected to a BATTERY (105) with terminals BATT.A and BATT.B. The adapter also includes a host interface (13) connected to a HOST PMU (12) via a HOST BUS (22). The adapter control bus (133) is connected to the adapter control bus (130). The adapter also includes a selector circuit (409) with an IAD sense amp (15) and a dec. circuit (411) with an IAD sense amp (16).



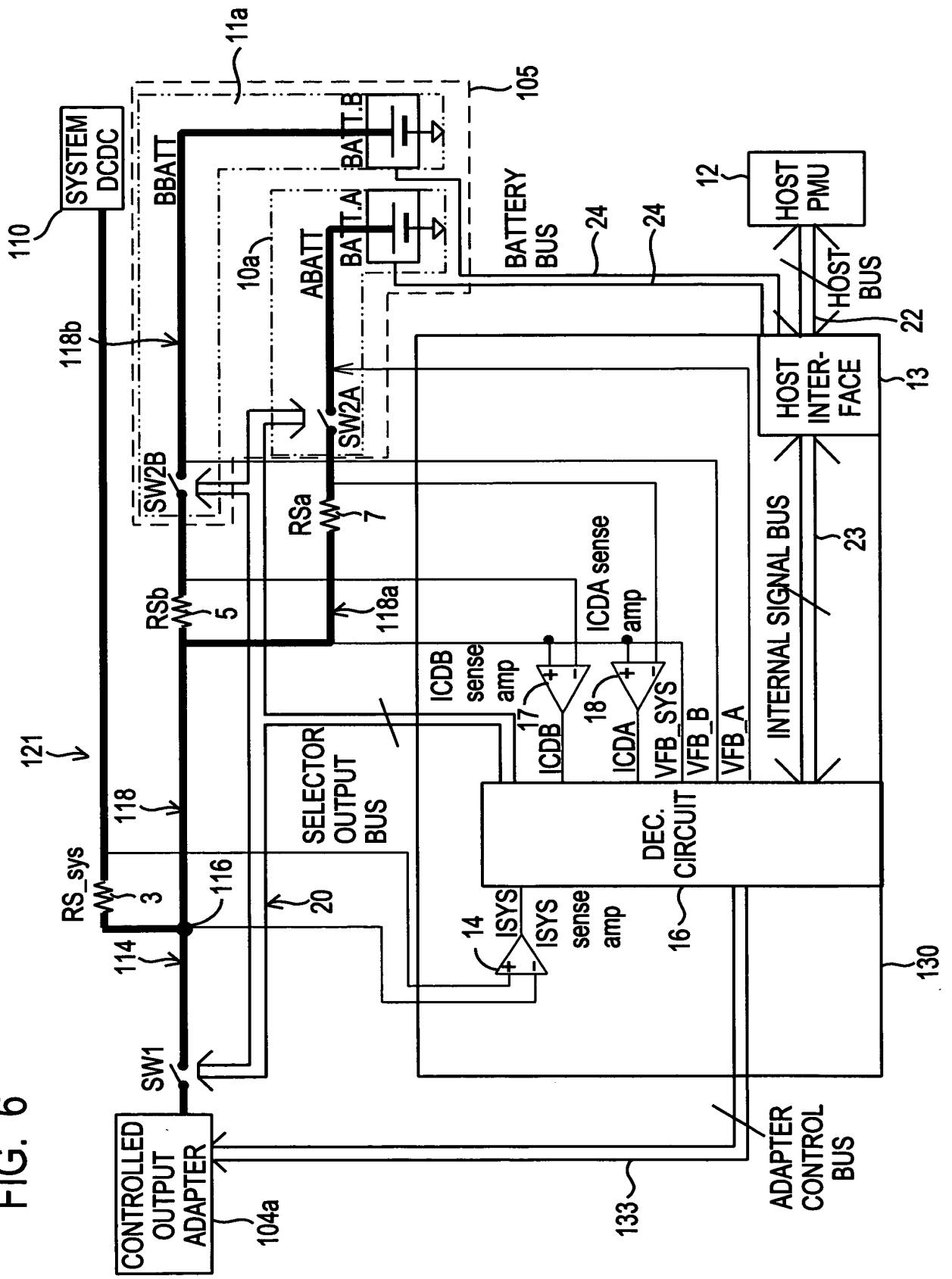
[illegible]

FIG. 7

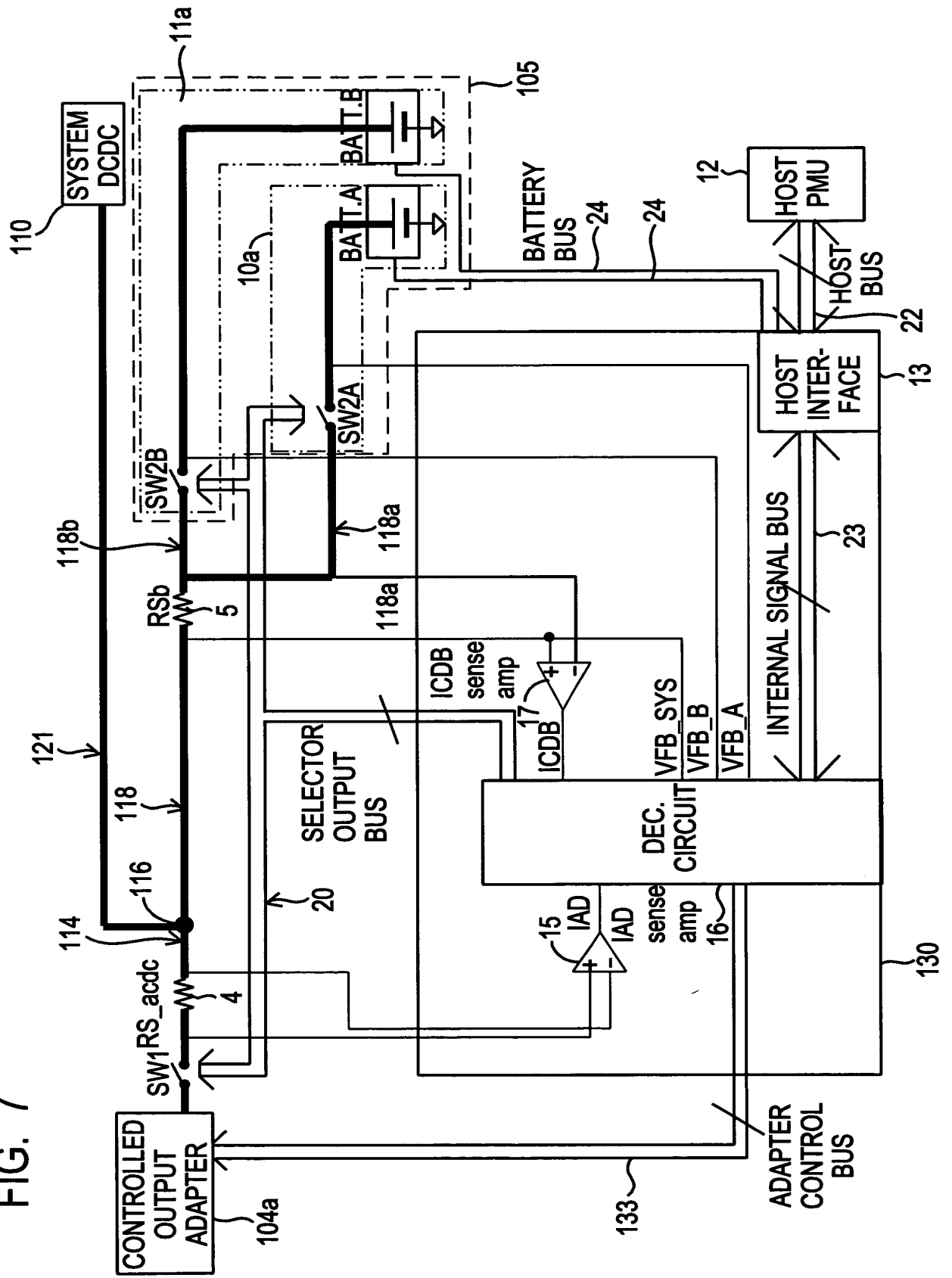


Fig. 8

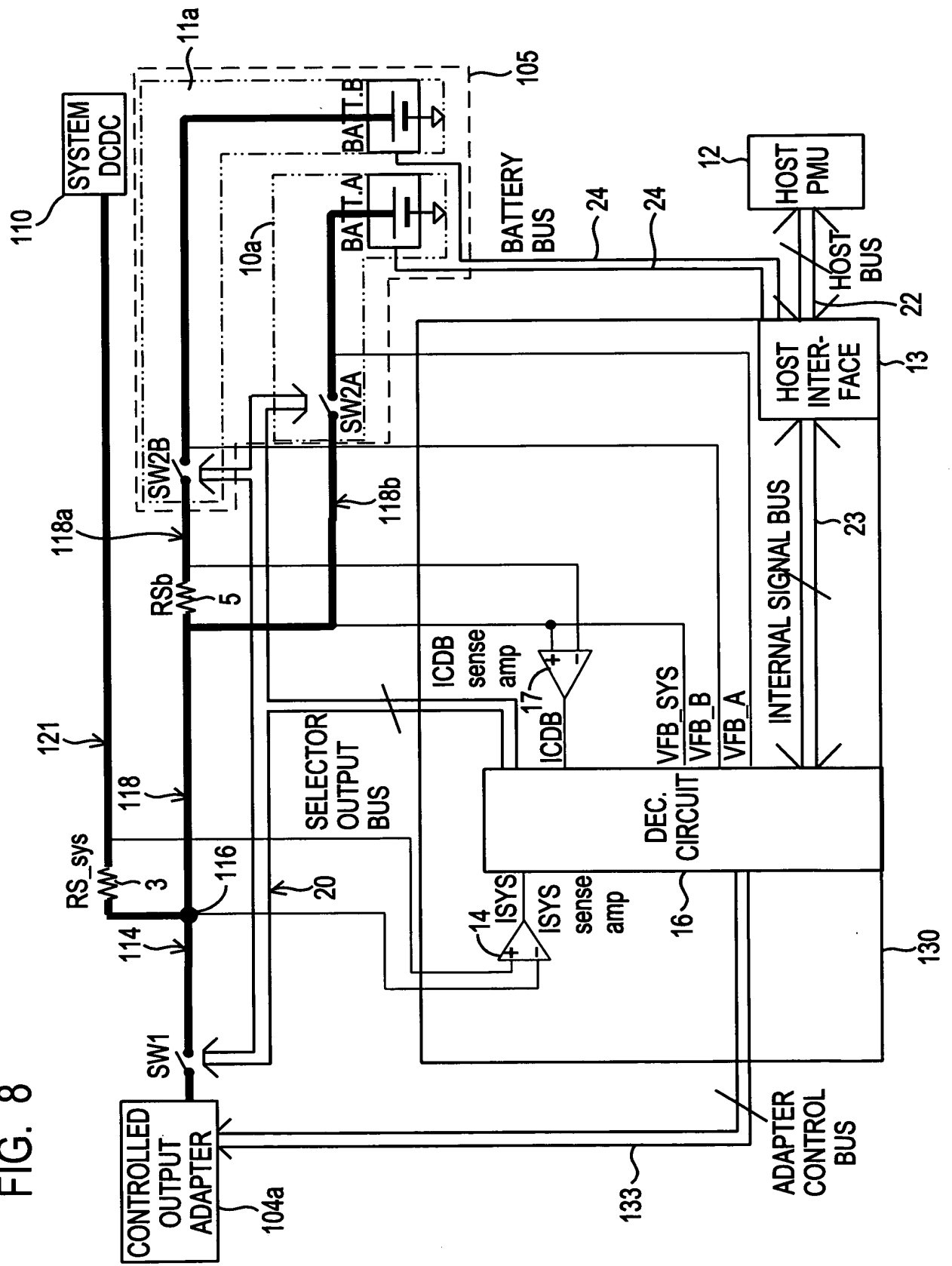


FIG. 9

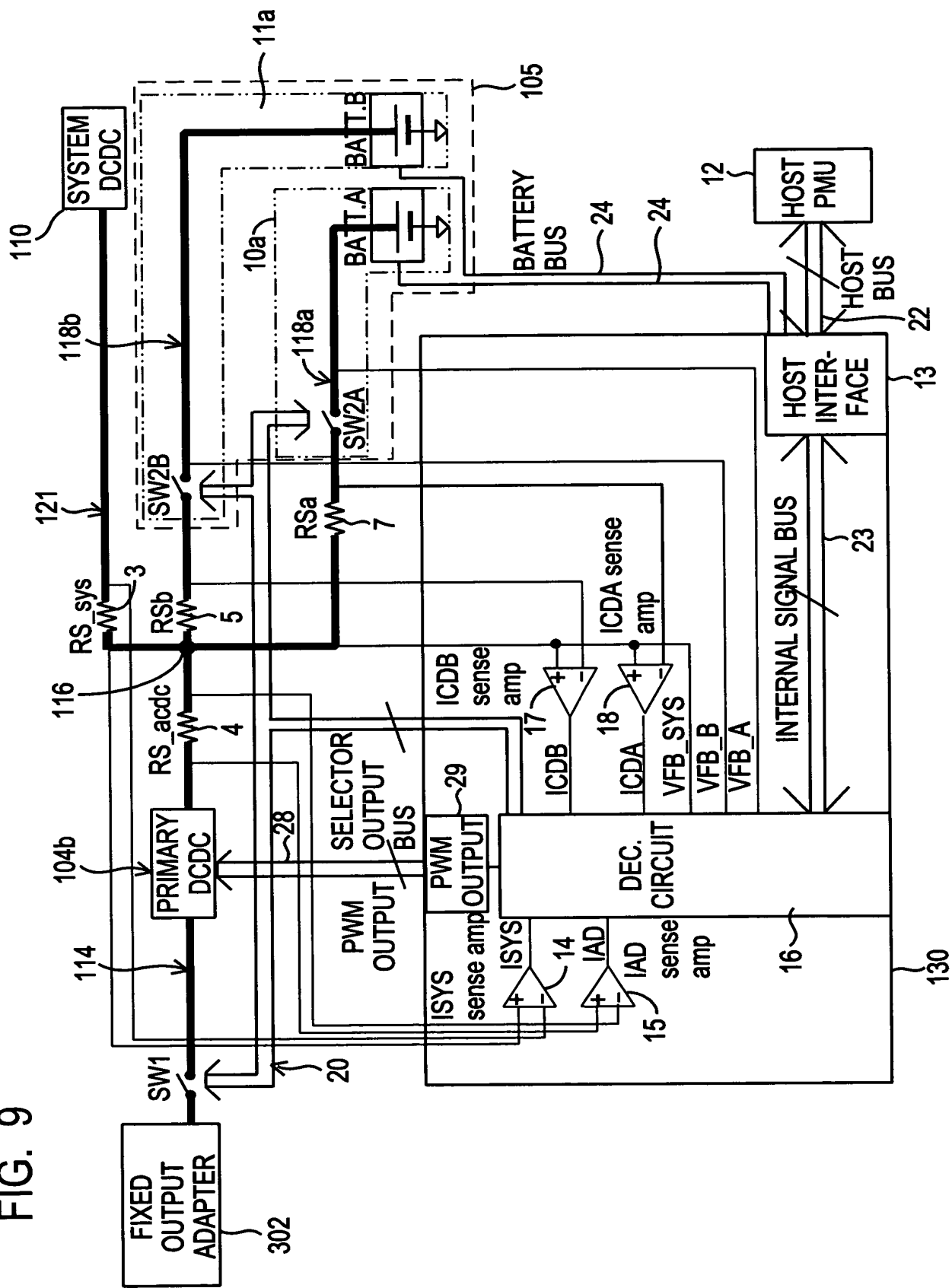


FIG. 10

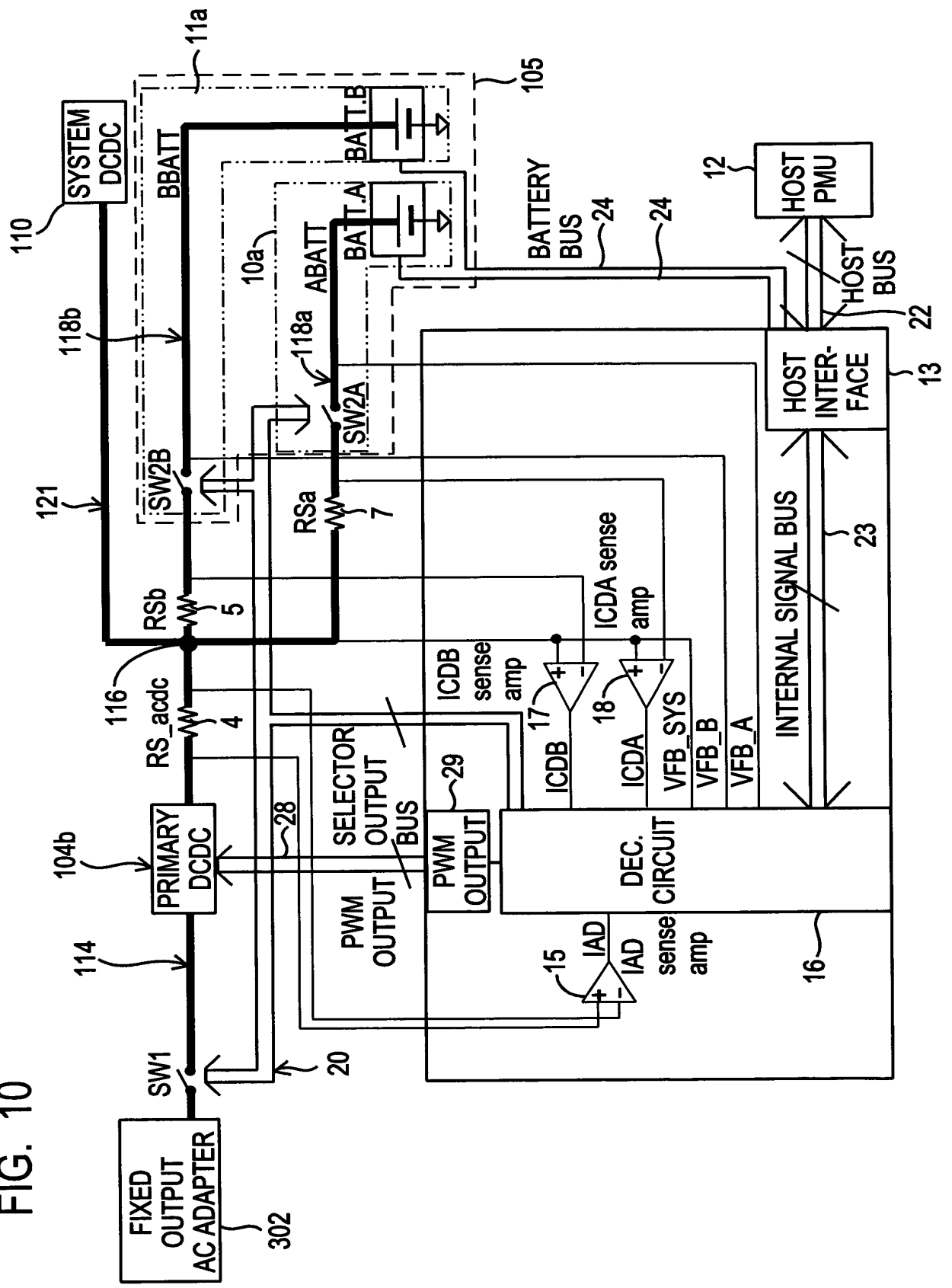


FIG. 11

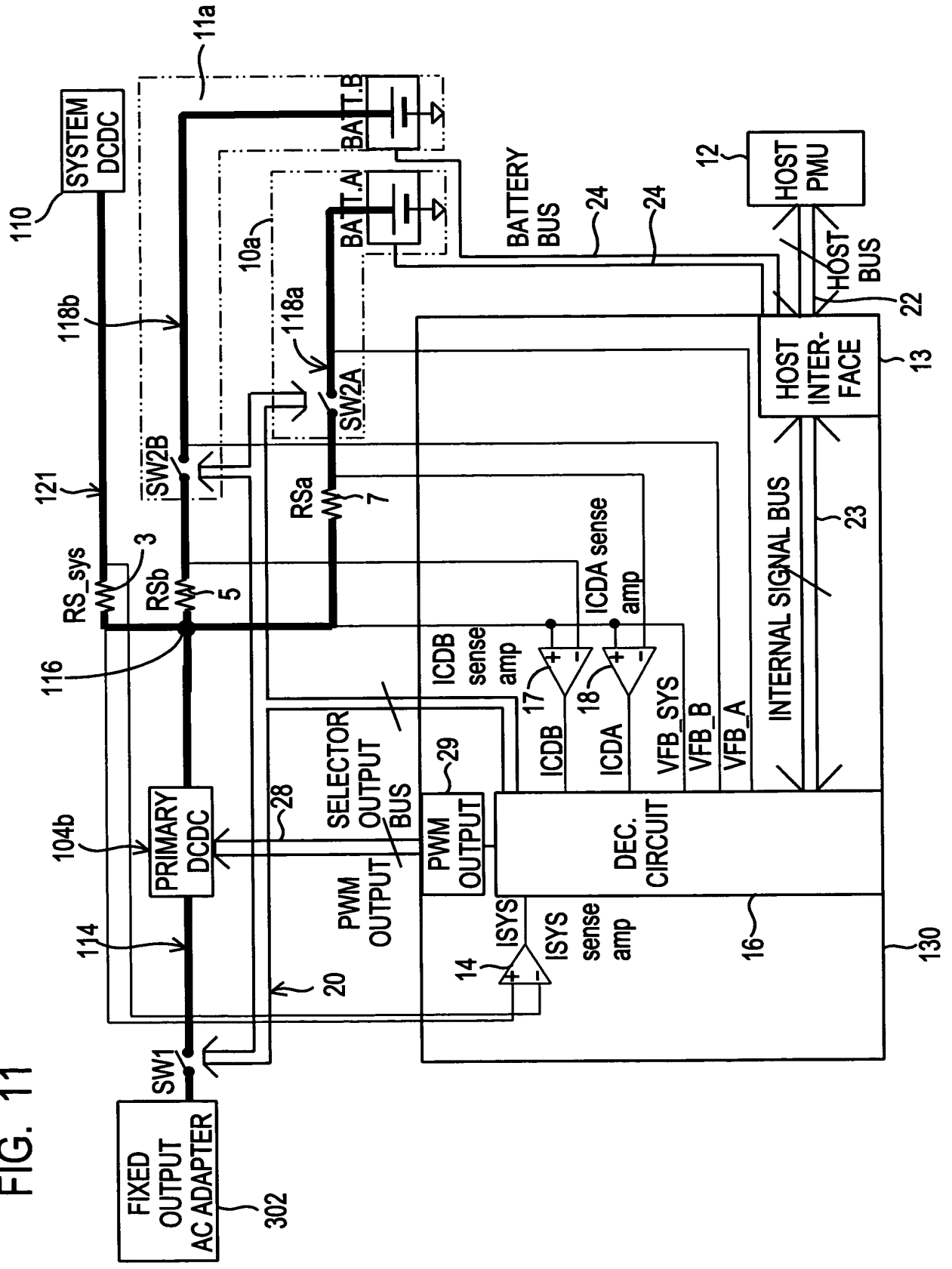


FIG. 12

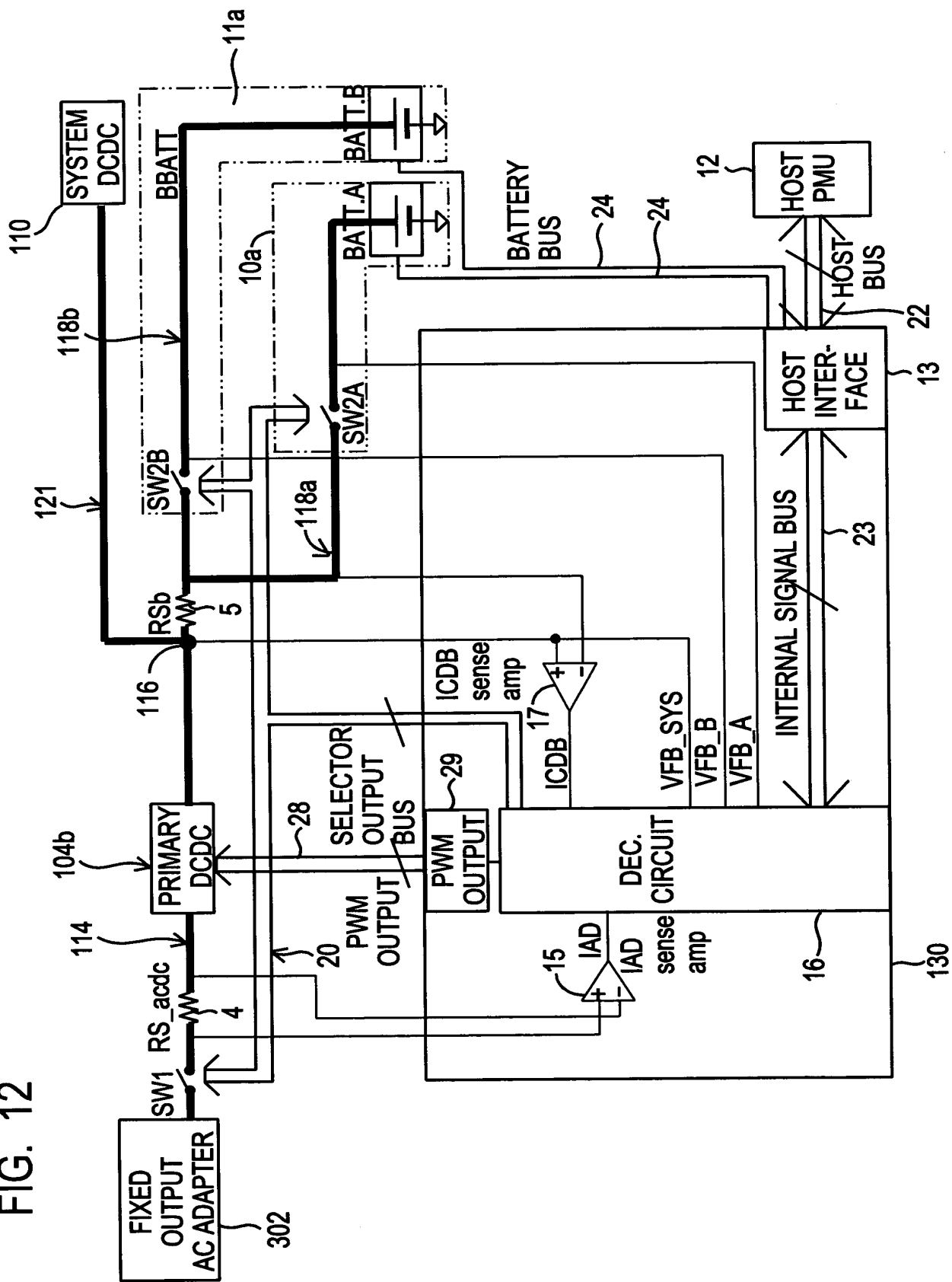


FIG. 13

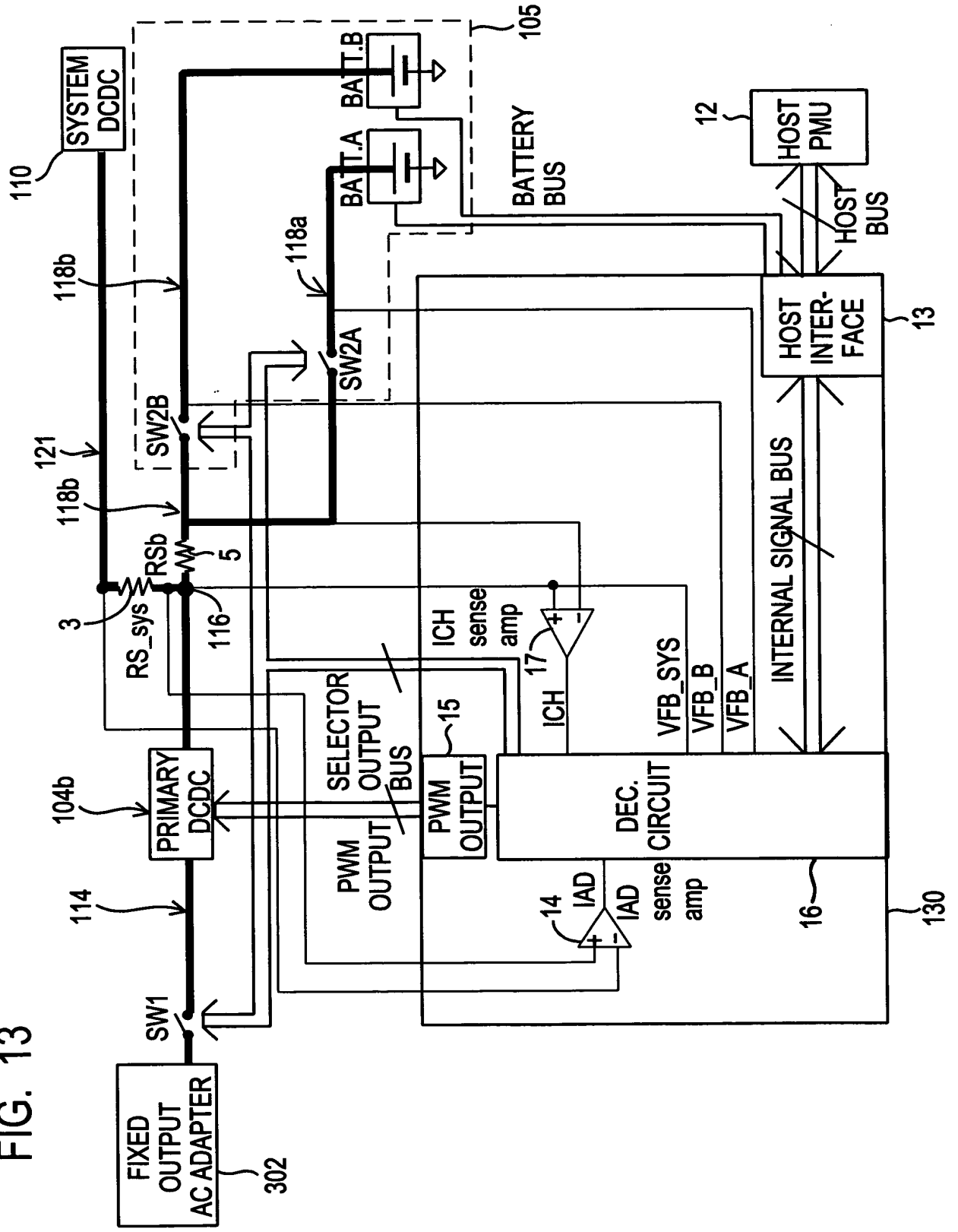


FIG. 14

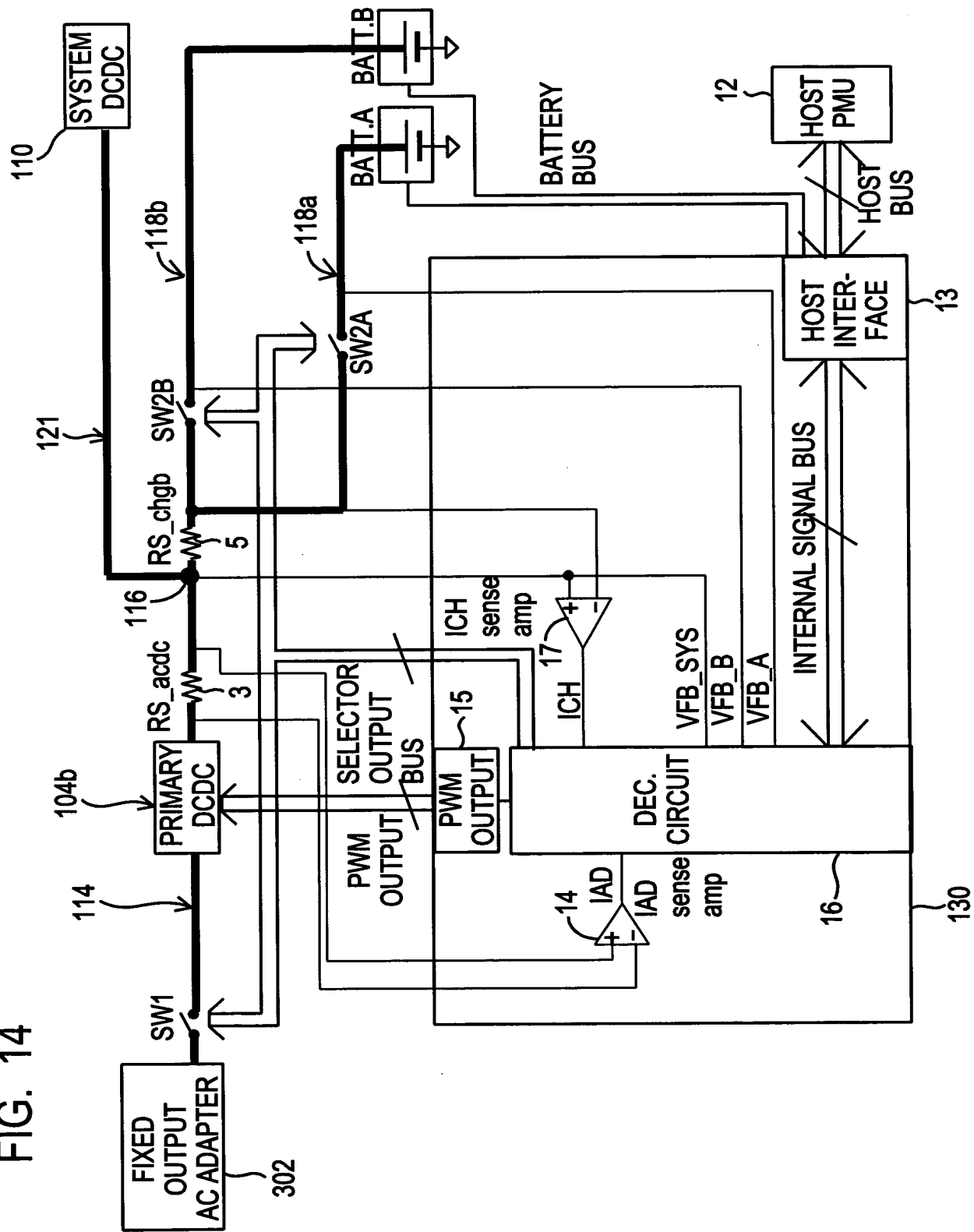


FIG. 15

